

Repeat Cycle Schedule of SIP

Department of Electrical Engineering

Time	01/07/2024	02/07/2024	03/07/2024	04/07/2024	05/07/2024
9:00 - 12.00 Hrs	OTDR	PLL	Assembly Language	QBOT	Digital Modulation
	Thyristor	Invertor Pendulum	PLC	VLAN	
	Analog to Digital			Simulating the Production of Electrical Power	
	Study of the Parallel Connection between two Synchronous Generators				
	Motor Controlling (Only for Mechanical Students)				
13.15 - 16.15 Hrs		DC Compound Motor	VOIP	TV Receiver	
			Fluorescent Lamp	Protection Relays	